

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2213	"and" same "xOR" same operation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 09:55
L2	3	"and" same "xOR" same operation same cad	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 09:54
L3	983	"and" same overlap\$4 same operation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 09:55
L4	26	("and" adj3 function) same overlap\$4 same operation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 09:56
L5	3	("and" adj3 function) same overlap\$4 same pattern	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 10:28
L6	53638	overlap\$4 same pattern	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 10:28
L7	860	overlap\$4 same pattern same layout	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 10:29
L8	2	overlap\$4 same pattern same layout same "and" same operation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 10:31

L9	0	(non adj overlap\$4) same pattern same layout same operation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 10:32
L10	6	"xor" same pattern same layout same operation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 10:35
L11	59	"xor" same layout same operation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 10:56
L12	132	(active adj layer) same (gate adj layer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 10:57
L13	24	(active adj layer) same (gate adj layer) same overl\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 10:57
S1	101	(convert\$3 or transform\$3 or translat\$3) same (planar adj3 transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 09:53
S2	2	(convert\$3 or transform\$3 or translat\$3) same (planar adj3 transistor) same (double adj2 transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/03 14:19
S3	26	(planar adj3 transistor) same (double adj2 transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/03 14:29

S4	4	(planar adj3 transistor) same (double-gate adj transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/03 14:26
S5	77	(double-gate adj transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/03 14:26
S6	1	S1 and S5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/03 14:26
S7	6791	double and gate adj transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/03 14:27
S8	210	double adj gate adj transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/03 14:27
S9	1	S1 and S8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/03 14:27
S10	7	fin and S3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/03 14:29